

Low Profile, High Current Dual Inductors



ADDITIONAL RESOURCES



STANDARD ELECTRICAL SPECIFICATIONS

L_0 INDUCTANCE $\pm 20\%$ AT 100 kHz, 0.25 V, 0 A (μ H)	DCR TYP. 25 °C (m Ω)	DCR MAX. 25 °C (m Ω)	HEAT RATING CURRENT DC TYP. (A) ⁽¹⁾	SATURATION CURRENT DC TYP. (A) ⁽²⁾	SRF TYP. (MHz)
10	30.5	32.6	5.6	7.4	10.2
15	43.5	45.5	4.6	5.5	9.5
22	67.8	72.5	4.1	4.1	7
33	100	107.0	3.8	3.7	6

Notes

- All test data is referenced to 25 °C ambient
- Operating temperature range -55 °C to +155 °C
- The part temperature (ambient + temp. rise) should not exceed 155 °C under worst case operating conditions. Circuit design, component placement, PWB trace size and thickness, airflow and other cooling provisions all affect the part temperature. Part temperature should be verified in the end application
- Rated operating voltage (across inductor) = 50 V
- ⁽¹⁾ DC current (A) that will cause an approximate ΔT of 40 °C
- ⁽²⁾ DC current (A) that will cause L_0 to drop approximately 20 %

FEATURES

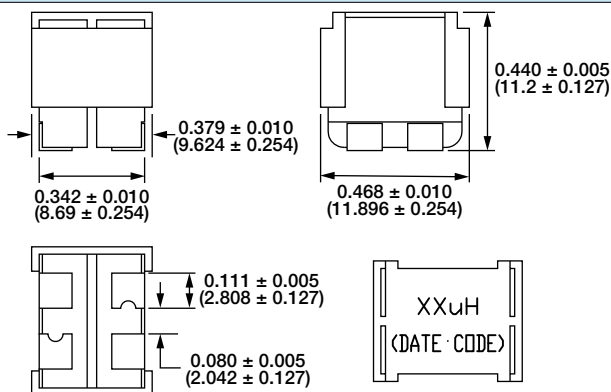
- Two inductors in one package
- High temperature, up to 155 °C
- Shielded construction
- Optimal design realizes high quality sound and low distortion
- Low coupling for minimal cross-talk between inductors
- Frequency range up to 1 MHz
- Lowest DCR/ μ H, in this package size
- Handles high transient current spikes without saturation
- Ultra-low buzz noise, due to composite construction
- AEC-Q200 qualified
- IHLP design. PATENT(S): www.vishay.com/patents
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912


RoHS
COMPLIANT

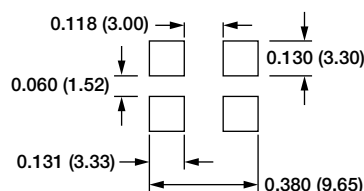
APPLICATIONS

- Class D audio amplifiers

DIMENSIONS in inches [millimeters]



RECOMMENDED PAD LAYOUT



DESCRIPTION

IHLD-4032KB-5A	22 μH	$\pm 20\%$	ER	e3
MODEL	INDUCTANCE VALUE	INDUCTANCE TOLERANCE	PACKAGE CODE	JEDEC® LEAD (Pb)-FREE STANDARD

GLOBAL PART NUMBER

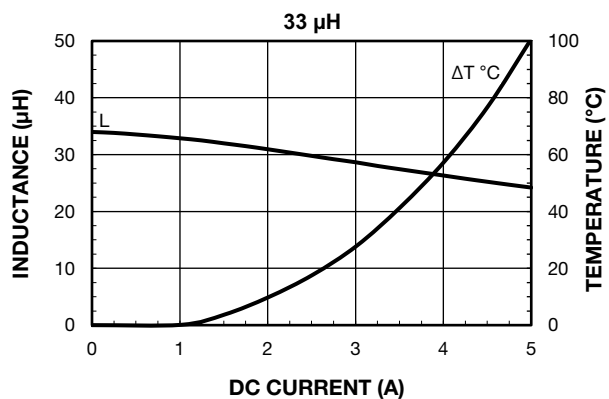
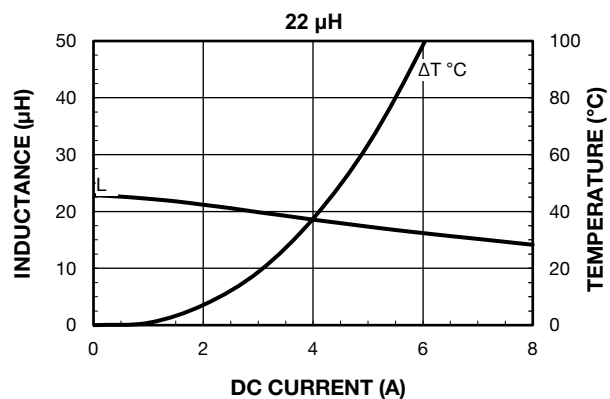
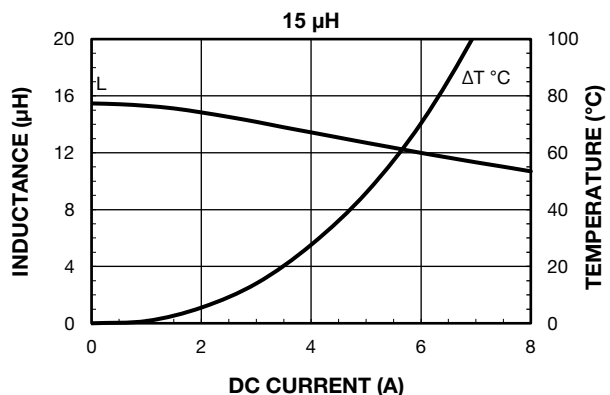
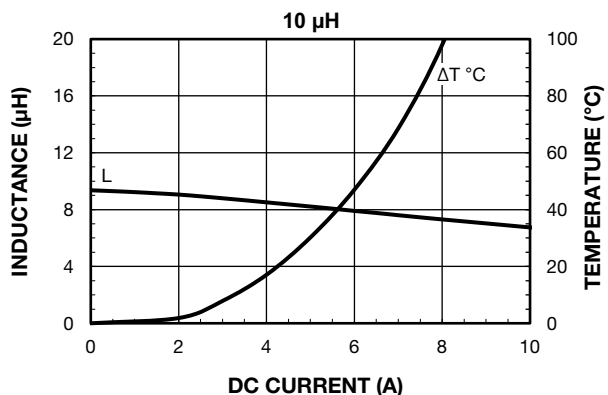
I	H	L	D	4	0	3	2	K	B	E	R	2	2	0	M	5	A
PRODUCT FAMILY				SIZE						PACKAGE CODE		INDUCTANCE VALUE			TOL.	SERIES	

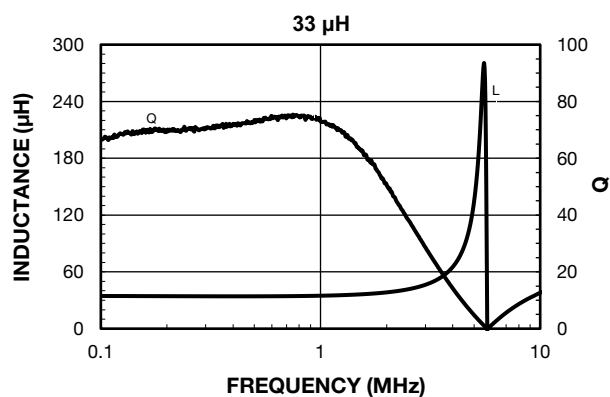
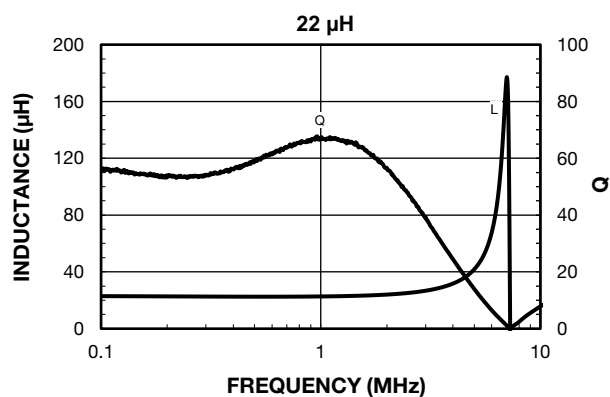
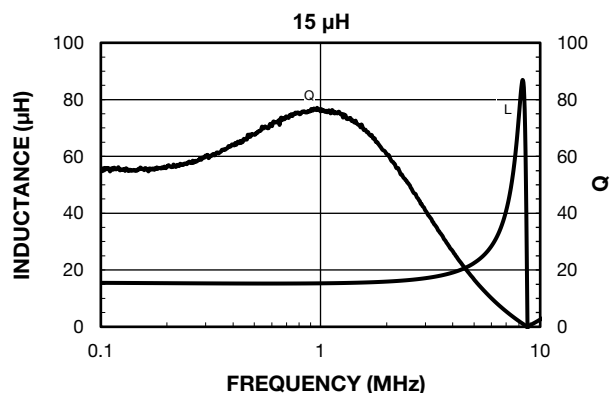
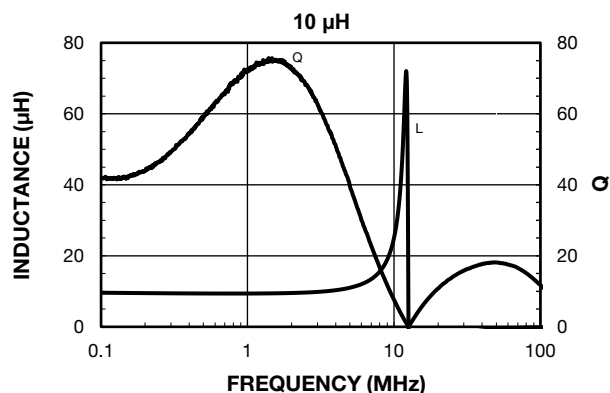
PATENT(S): www.vishay.com/patents

This Vishay product is protected by one or more United States and international patents.



PERFORMANCE GRAPHS



PERFORMANCE GRAPHS: INDUCTANCE AND Q VS. FREQUENCY


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 - Go to “Edit” → “Preferences” → “3D & Multimedia” → and mark “Enable playing of 3D content” → confirm with “OK”

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