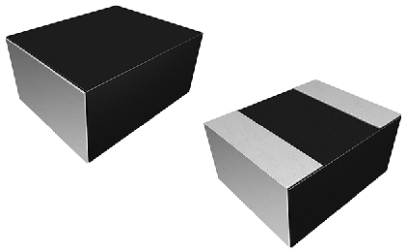
Low Profile, High Current Inductors



FEATURES

- Magnetic alloy power choke coil
- Miniature size (2.5 x 2.0) and low profile
- Magnetic shielded
- October 10, 2011 Low acoustic noise and high efficiency
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

Pb-free

RoHS COMPLIANT

HALOGEN FREE

APPLICATIONS

- PDA / notebook / desktop / server applications
- High current POL converters
- Low profile, high current power supplies
- Battery powered devices
- DC/DC converters in distributed power systems
- DC/DC converter for field programmable gate array (FPGA)

STANDARD ELECTRICAL SPECIFICATIONS

L_0 INDUCTANCE ± 20 % AT 100 kHz, 0.25 V, 0 A (µH)	DCR 25 °C (mΩ)		HEAT RATING CURRENT DC (A) (3)		SATURATION CURRENT DC (A) (4)	
	TYP.	MAX.	TYP.	MAX.	TYP.	MAX.
0.22	8.4	10.5	7.40	6.60	7.10	6.50
0.24	11	13	6.40	5.70	6.60	6.00
0.33	14	17	5.60	5.00	6.10	5.50
0.47	21	26	4.50	4.00	5.05	4.55
1.0	40	48	3.10	2.70	3.90	3.50
1.5	57	69	2.70	2.43	3.10	2.80
2.2	79	95	2.30	2.00	2.60	2.30

Notes

(1) All test data is referenced to 25 °C ambient.

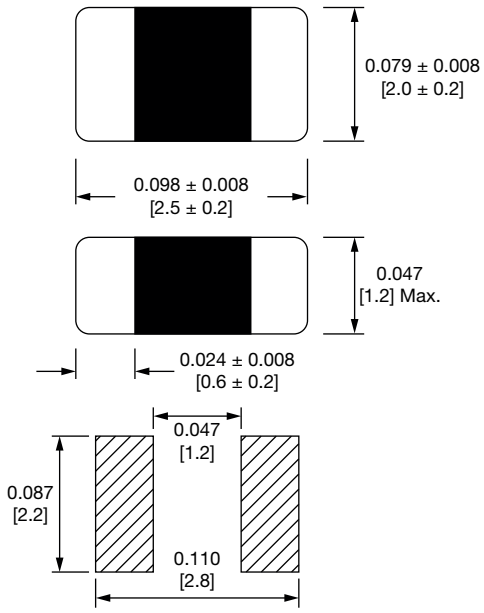
(2) Operating temperature range -55 °C to +125 °C .

(3) DC current (A) that will cause an approximate ΔT of 40 °C.

(4) DC current (A) that will cause L_0 to drop approximately 30 %.

(5) The part temperature (ambient + temp. rise) should not exceed 125 °C under worst case operating conditions. Circuit design, component placement, PWB trace size and thickness, airflow and other cooling provisions all affect the part temperature. Part temperature should be verified in the end application.

DIMENSIONS in inches [millimeters]



DESCRIPTION

IHHP-1008AB-01	1.0 µH	± 20 %	ER	e3
MODEL	INDUCTANCE VALUE	INDUCTANCE TOLERANCE	PACKAGE CODE	JEDEC® LEAD (Pb)-FREE STANDARD

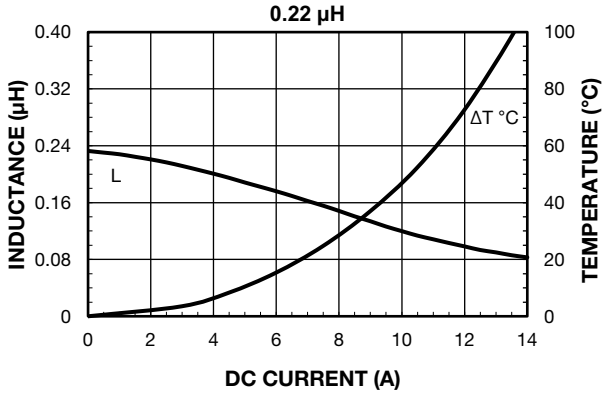
GLOBAL PART NUMBER

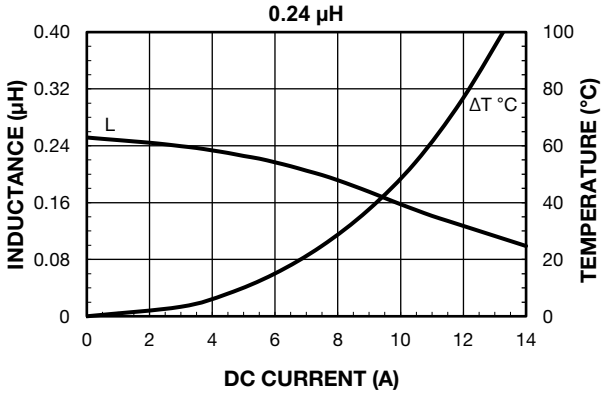
I	H	H	P	1	0	0	8	A	B	E	R	1	R	0	M	0	1
PRODUCT FAMILY				SIZE						PACKAGE CODE		INDUCTANCE VALUE			TOL.	SERIES	

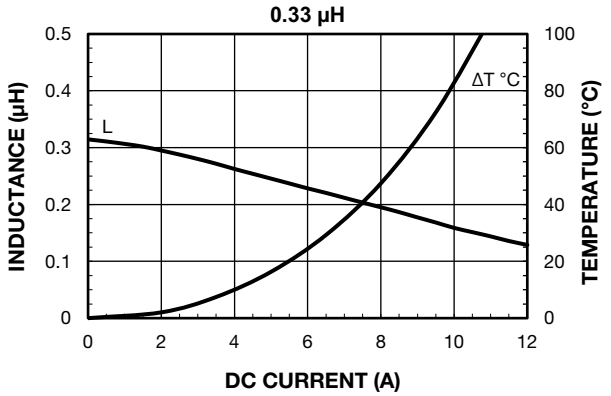
For technical questions, contact: magnetics@vishay.com

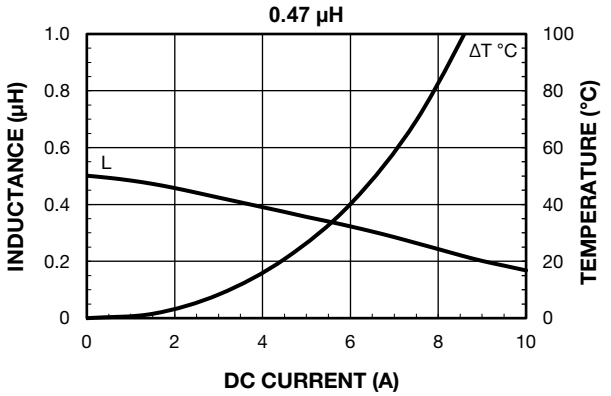
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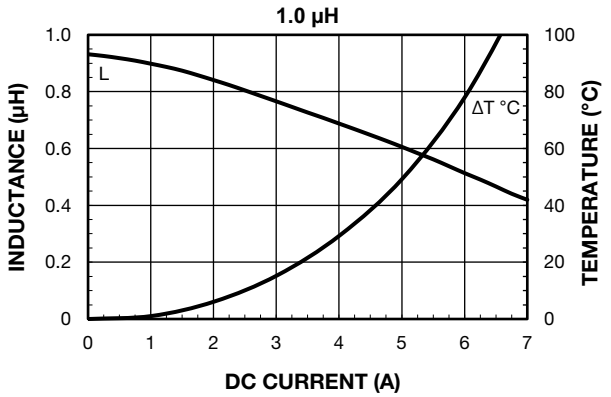
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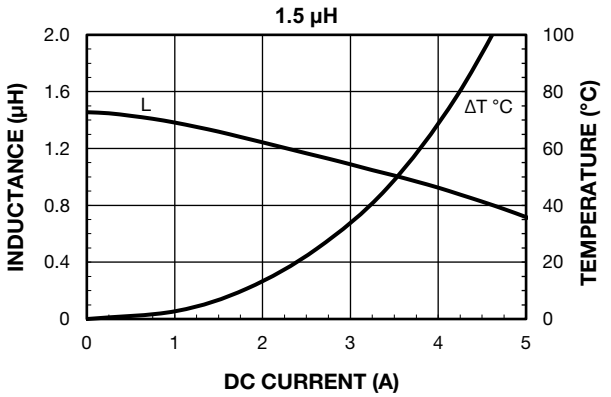


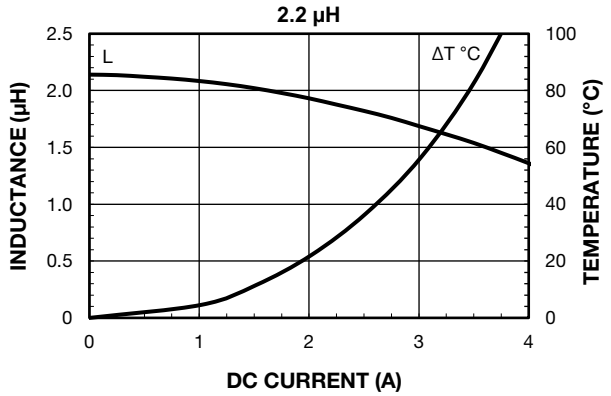








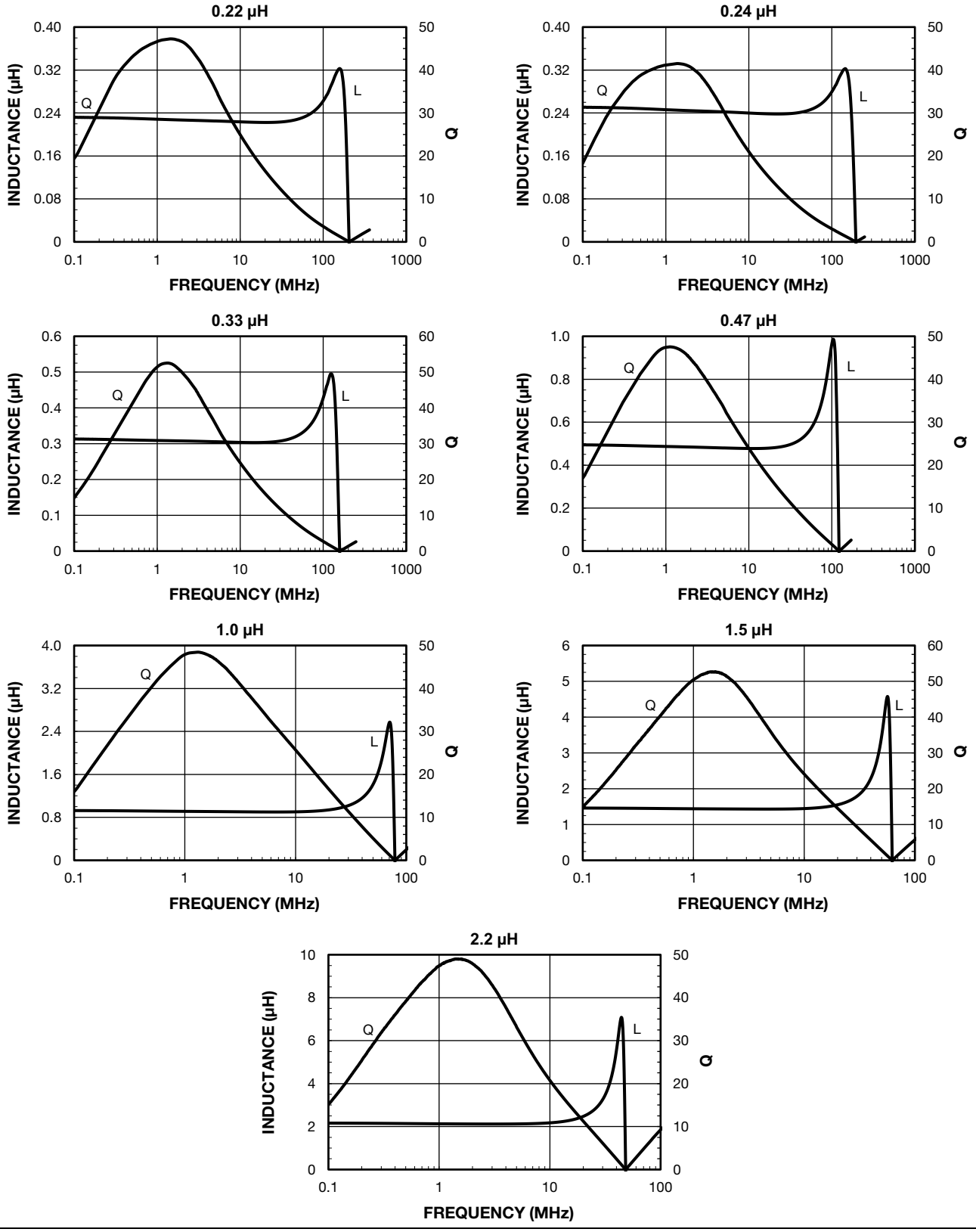




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