CUSTOMER: _____ DATE: _____

APPROVAL SPECIFICATION

RoHS+HSF
COMPLIANT

PRODUCT NAME: SMD power inductor

YOUR PART NO.:

OUR PART NO.: MPIE201610 Series

VERSION: V2.0

RECEPTION THE SPECIFICATION HAS BEEN ACCEPTED. **COMPANY:** DATE:		
CFMD	CHKD	RCVD

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MANUFACTURING NAME

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CATALOG

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Component SPEC Version Record

Rev.	Effective Date	Changed Contents	Change Reasons	Approved By
1.0	Dec. 10.2012	New released	/	Charles
2.0	JUL. 31.2014	Change the Electrical Characteristics	The Electrical Characteristics improved	Charles

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1. Scope

This specification applies to the MPIE201610 series of SMD power inductor.

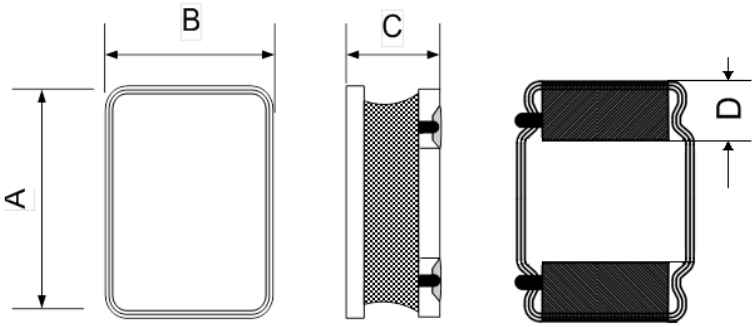
2. Product Identification

MPIE	201610	-	4R7	M	-	LF
①	②		③	④		⑤

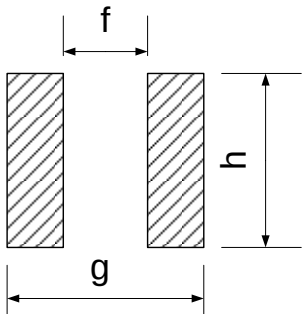
① Product Symbol (E type SMD power inductor)
② Product dimensions (2.0×1.6×1.0mm)
③ Inductance Value： (R68:0.68uH; 4R7:4.7uH;330:33uH)
④ Inductance Tolerance：(M: ±20% ; N: ±30%)
⑤ Lead free product.

3. Appearance, Dimensions and Material

3.1 Appearance and dimensions



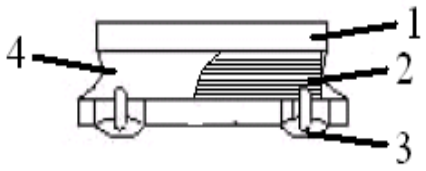
Pictures are for reference only



Recommended Land Pattern

Dimensions in mm						
A	B	C	D	f	g	h
2.00±0.20	1.60±0.20	1.00Max.	0.65±0.2	0.80 Typ.	2.20 Typ.	1.60 Typ.

3.2 Material List



No.	Item	Material
1	Core	Ni-Zn Ferrite
2	Wire	Enameled Copper Wire
3	Terminal Electrode	Ag/Ni/Sn/Cu
4	Magnetic Glue	Epoxy resin and magnetic powder

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4.Testing Conditions

Unless otherwise specified, the standard conditions for measurement/test as:
Ambient Temperature : 5 to 35℃
Relative Humidity: 25 to 85% RH
Atmospheric Pressure: 86 to 106 kPa

If any doubt on the results, measurements/tests should be made within the following limits:
Ambient Temperature : 25±1℃
Relative Humidity: 60 to 70% RH
Atmospheric Pressure: 86 to 106 kPa

5. Electrical Characteristics And Test Instruments

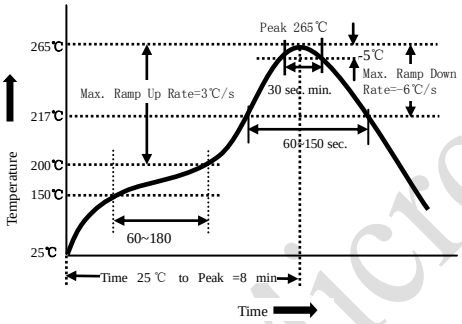
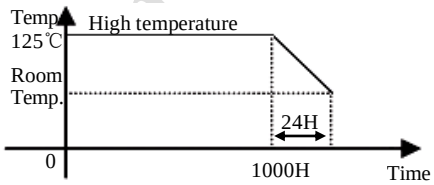
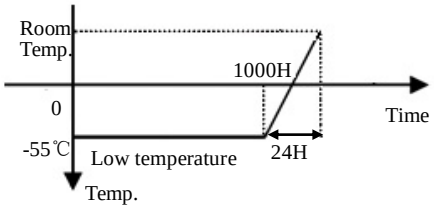
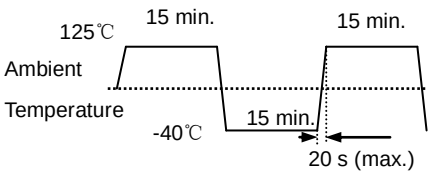
Microgate Part No.	Inductance L (uH) 1MHz/1V	DCR(Ω)		Isat[•1] (A)		Irms[•2] (A)	
		Max.	Typ.	Max.	Typ.	Max.	Typ.
MPIE201610-R33M-LF	0.33±20%	0.026	0.022	3.20	3.50	4.50	5.00
MPIE201610-R47M-LF	0.47±20%	0.040	0.035	3.40	3.60	2.80	3.00
MPIE201610-R68M-LF	0.68±20%	0.055	0.045	3.00	3.20	2.60	3.00
MPIE201610-1R0M10-LF	1.0±20%	0.090	0.080	2.50	2.70	2.50	2.60
MPIE201610-1R5M-LF	1.5±20%	0.120	0.100	1.50	1.70	1.70	2.00
MPIE201610-2R2M-LF	2.2±20%	0.150	0.125	1.50	1.60	1.50	1.60
MPIE201610-3R3M-LF	3.3±20%	0.330	0.275	1.30	1.50	1.02	1.12
MPIE201610-4R7M-LF	4.7±20%	0.350	0.265	1.00	1.10	1.00	1.10
MPIE201610-100M-LF	10±20%	0.815	0.680	0.68	0.75	0.61	0.70
MPIE201610-150M-LF	15±20%	1.440	1.200	0.40	0.50	0.40	0.45
MPIE201610-220M-LF	22±20%	1.750	1.460	0.50	0.53	0.36	0.40

Test instruments and remarks

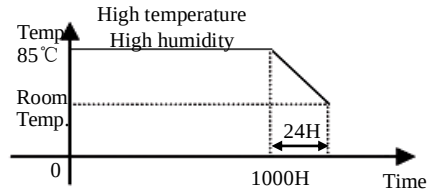
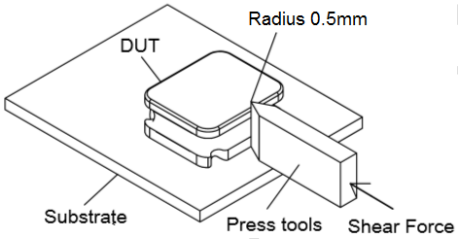
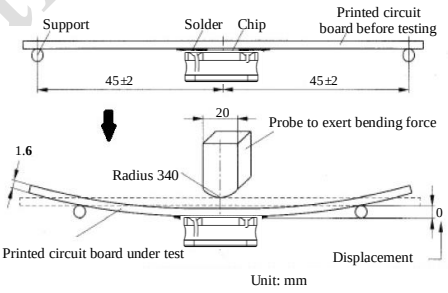
* All test data is referenced to 25℃ ambient.
* L test by CHROMA 3302 meter or equivalent.
* DCR test by Tonghui TH2516B meter or equivalent.
* CHROMA 3302 and 1320 meter for IDC.
* Isat: DC current (A) that will cause L0 to drop approximately 30%.
* Irms: DC current (A) that will cause an temperature rise ΔT approximate to 40℃.
* The rated current as listed is either the saturation current or the heating current depending on which value is lower.
* Operating temperature: -40℃ to +125℃(Including self-heating)
* The part temperature (ambient + temp rise) should not exceed 125℃ under worse case operating conditions. Circuit design, component placement, PCB trace size and thickness, airflow and other cooling provision all affect the part temperature. Part temperature should be verified in the end application.

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6. Reliability

No.	Item	Requirements	Test Methods and Remarks	Reference	Sample Size
1	Solderability	(1) No physical damage. (2) Terminal area must have 95% min. solder coverage.	①Temperature:245±5℃, flux 5-10 s. ②Sample immersion tin furnace 5 ±0.5s. ③Immersed and in and out of speed: 25 ±6mm/s.	AEC-Q200 (J-STD-002)	15
2	Resistance to Soldering Heat	(1) No physical damage. (2)｜ΔL0/L0｜≤10%	①The peak temperature: 260+5/-0℃. ②Reflow:3times. ③Temperature curve is as below: 	AEC-Q200 (MIL-STD-202 Method 210)	30
3	High Temperature Storage		①Temperature: 125±2℃. ②Time : 1000 hours. ③Measurement at 24±4 hours after test conclusion. 	AEC-Q200 (MIL-STD -202 Method 108)	77
4	Low Temperature Storage		①Temperature: -55±2℃. ②Time : 1000 hours. ③Measurement at 24±4 hours after test conclusion. 	JESD22-A119	77
5	Thermal shock		①First -40℃ for 15 minutes, last 125℃ 15minutes as 1 cycle. Go through 300 cycles. ②Max transfer time is 20 second. ③Measurement at 24±4 hours after test conclusion. 	MIL-STD -202 Method 107	30

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No.	Item	Requirements	Test Methods and Remarks	Reference	Sample Size
6	Humidity Resistance	(1) No physical damage. (2) |ΔL0/L0| ≤10%	①1000 hours, 85℃/85%RH. ②Unpowered. ③Measurement at 24±4 hours after test conclusion. 	AEC-Q200 (MIL-STD -202 Method 103)	77
7	Terminal Strength		①The test samples shall be soldered to the board. ②17.64N, 60s. 	AEC-Q200 (AEC-Q200-0 06)	30
8	Board Flex		①Part mounted on a 100mm*40mm FR4 PCB board, which is 1.6±0.2 mm thick and as a Layer-thickness 35 μm ±10 μm. ②Bending speed is 1mm/s. ③Keeping the P.C Board 2 mm minimum for 60 seconds. 	AEC-Q200 (AEC-Q200-0 05)	30
9	Drop		①Height: 1 m, Free fall, 10times. ②Direction: 1 Angle, 1side, 2surface.	JESD22-B111	30
10	Vibration		①Frequency range : 10~2000Hz. ②Amplitude: 1.5mm , 5g. ③Sweep time and duration: 10~2000~10Hz for 20 minutes. ④Each four hours(12 times) in X,Y,Z direction, 12 hours in total.	AEC-Q200 (MIL-STD-20 2 Method 204)	30

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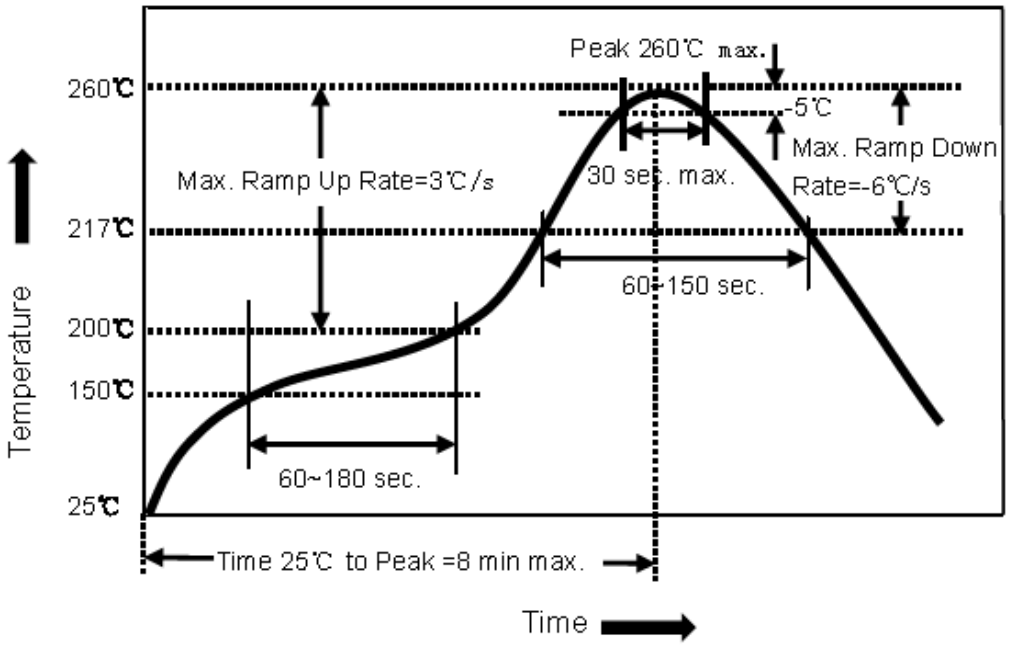
No.	Item	Requirements	Test Methods and Remarks	Reference	Sample Size
11	Loading at High Temperature	(1) No physical damage. (2) |ΔL0/L0| ≤10%	①Temperature: 85±2℃. ②Time : 1000 hours. ③Applied Current : Rated current. ④Measurement at 24±4 hours after test conclusion.	AEC-Q200 (MIL-PRF-27)	77

*All above experiments items need 3 Lot., sample size is as specified in the table above.

*Sample size standard is from AEC-Q200 : qualification sample size requirements.

7. Recommended Soldering Conditions

(1) Reflow soldering conditions



*Above reflow soldering curve is from J-STD-020D.

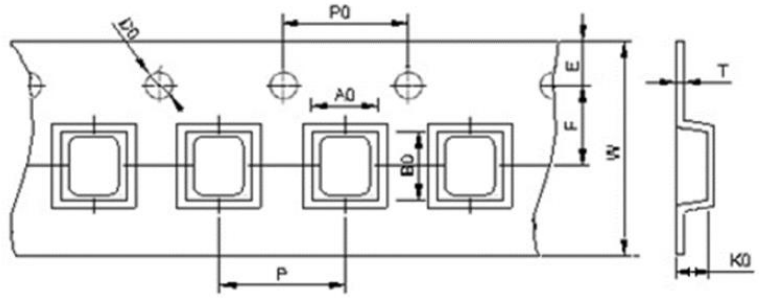
(2) Iron soldering

The following conditions must be strictly followed when using a soldering iron.

Pre-heating	150℃ 1 minute
Tip temperature	350℃ max
Soldering iron output	30w max
End of soldering iron	Φ1mm max
Soldering time	3 seconds max

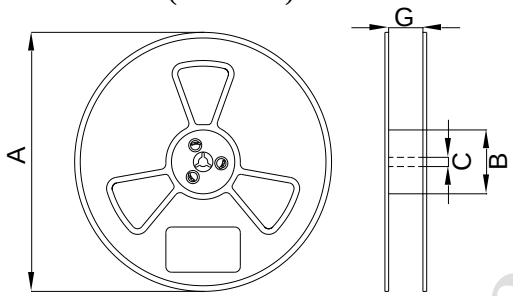
8. Package Information

(1) Dimension of tape (Unit: mm)



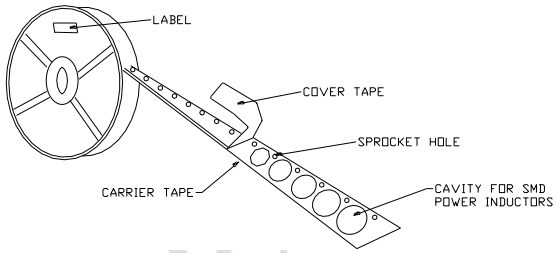
W	A0	B0	K0	E	F	P	P0	D0	T
8.0±0.3	1.90±0.1	2.30±0.10	1.20±0.1	1.75±0.1	3.5±0.05	4.0±0.1	4.0±0.1	1.5+0.1/-0.0	0.18±0.03

(2) Dimension of reel (Unit: mm)



Symbol	Dimension
A	178±2
B	58±2
C	13.5±0.2
G	9.00±0.5

(3) Taping figure and drawing direction

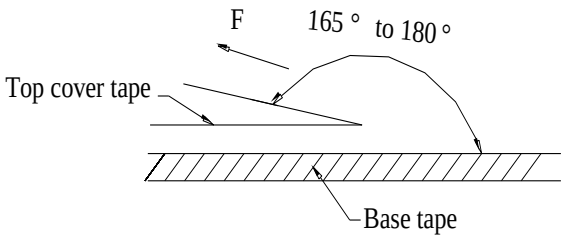


(4) Packaging quantities: 2000PCS/Reel.

(5) Peeling strength of cover tape:

The peel force of top cover tape shall be between 0.10N to 1.0N

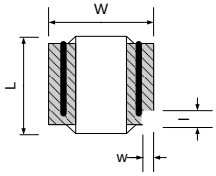
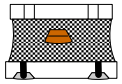
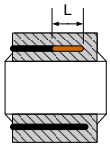
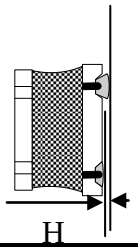
*the peel force standard is from EIA-481-D



Room Temp. (℃)	Room Humidity (%)	Room aim (hpa)	Peel Speed mm/min
5-35	45-85	860-1060	300

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9. Visual inspection standard of product

No.	Defect Item	Graphic	Rejection identification	Acceptance
1	Core defect		l >L/6 or w >W/6, NG.	AQL=0.65
2	Missing resin		The area of missing resin more than single face, NG	AQL=0.65
3	Cold solder		L more than 1 mm, NG.	AQL=0.65
4	Solder uneven		H>0.1mm. NG.	AQL=0.65

10. Products Storage

(1) Storage period

Solderability shall be guaranteed for 12 months from the date of manufacture on condition that they are stored at the environment specified in specification. For those parts, which passed more than the time shall be checked solder-ability before use.

(2) Storage conditions

Products should be storage in the warehouse on the following conditions:

Temperature: -10 ~+ 35℃

Humidity: Less than 70% relative and humidity

No rapid change on temperature and humidity.

(3) Don't keep products in corrosive gases such as sulfur, chlorine gas or acid, or it may cause oxidization of electrode, resulting in poor solderability.

(4) Products should be storage on the palette for the prevention of the influence from humidity, dust and so on.

(5) Products should be storage in the warehouse without heat shock, vibration, direct sunlight and so on.

(6) Products should be storage under the airtight packaged condition.

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