CUSTOMER: _____ **DATE:** _____

For Automotive Electronics

APPROVAL SPECIFICATION

RoHS+HSF
COMPLIANT

车载品

AEC-Q200

PRODUCT NAME:	SMD Wire Wound Chip Inductor
YOUR PART NO.:	
OUR PART NO.:	AMTF1210L Series
VERSION:	V1.1

RECEPTION **THE SPECIFICATION HAS BEEN ACCEPTED.** **COMPANY:** **DATE:**		
CFMD	**CHKD**	**RCVD**

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MANUFACTURING NAME

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Component SPEC Version Record

Rev.	Effective Date	Changed Contents	Change Reasons	Approved By
1.0	2018.12.04	New released	/	Remo
1.1	2025.3.13	Update electrical parameters	/	Liuwei

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1. Scope

This specification applies to the AMTF1210L series of SMD Wire Wound Chip Inductors.

2. Product Identification

AMTF	1210	L	4R7	M	S	T
①	②	③	④	⑤	⑥	⑦

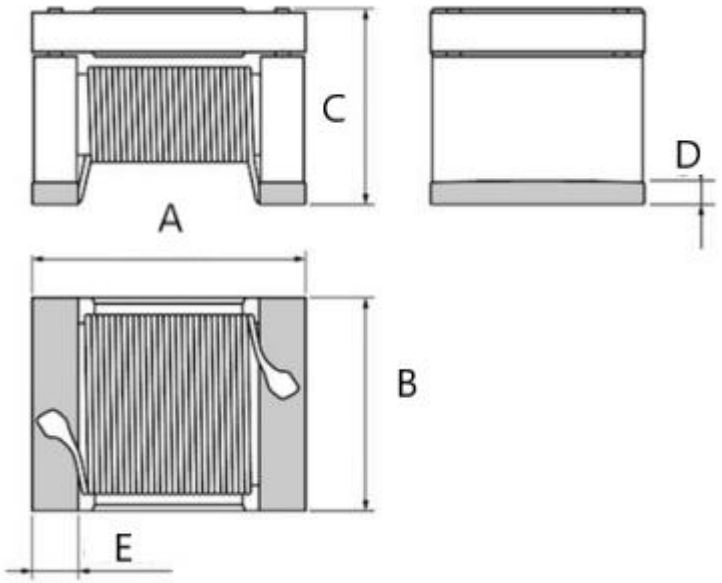
① Product Symbol(Automotive electronics products)
② Dimensions (1210 inch)
③ Features
④ Inductance Value (R65:0.65uH 4R7:4.7uH 470:47uH)
⑤ Inductance Tolerance

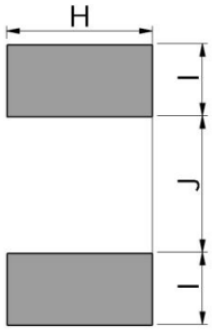
Code	J	K	M
Tolerance	±5%	±10%	±20%

⑥ Termination materials (G: gold ; S: sn)
⑦ Packaging style (T: Paper Taping ; B: Bulk; E: Plastic embossed)

3. Appearance, Dimensions and Material

(1) Appearance and dimensions





Recommended Land Pattern

Dimensions in mm							
A	B	C	D	E	H Typ.	I Typ.	J Typ.
3.2±0.2	2.5±0.2	2.3±0.2	0.3±0.1	0.58±0.1	2.5	0.9	2.2

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4. Testing Conditions

Unless otherwise specified, the standard conditions for measurement/test as:
Ambient Temperature : 5 to 35℃
Relative Humidity: 25 to 85% RH
Atmospheric Pressure: 86 to 106 kPa

If any doubt on the results, measurements/tests should be made within the following limits:
Ambient Temperature : 25±1℃
Relative Humidity: 60 to 70% RH
Atmospheric Pressure: 86 to 106 kPa

5. Rating

Operating temperature:-55℃~150℃

Microgate Part No.	Inductance (uH) @1MHz, 1V	Tolerance	DCR (Ω) Max.	Isat (mA) Max.	Imrs (mA) Max.	SRF (MHz.) Min.
AMTF1210L1R6MST	1.60	±20%	0.060	1500	1500	270
AMTF1210L2R2MST	2.20	±20%	0.190	1000	1000	200
AMTF1210L2R7MST	2.70	±20%	0.220	975	975	200
AMTF1210L3R3MST	3.30	±20%	0.240	950	950	150
AMTF1210L3R6MST	3.60	±20%	0.120	1000	1000	150
AMTF1210L3R9MST	3.90	±20%	0.260	900	900	120
AMTF1210L4R7MST	4.70	±20%	0.280	850	850	100
AMTF1210L6R8MST	6.80	±20%	0.240	800	1360	120
AMTF1210L100MST	10.0	±20%	0.400	500	700	100
AMTF1210L150MST	15.0	±20%	0.400	350	725	50
AMTF1210L220MST	22.0	±20%	0.620	400	550	50
AMTF1210L470MST	47.0	±20%	0.900	300	500	50

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6. Electrical Performance

Inductance；

Inductance shall meet item 5 when measured on the condition of Table 1.

Table 1

Measuring Equipment	Impedance analyzer keysight E4982A or equivalent
Measuring Frequency	In the table
Measuring signal level	-13dBm
Measuring Fixture	keysight 16197A

DCR

D.C Resistance shall meet item 5 when measured on the condition of Table 2.

Table 2

Measuring Equipment	LCR Meter HIOKI 3542 or equivalent

Self-Resonant Frequency (S.R.F)

S.R.F. shall meet item 5 when measured on the condition of Table 3.

Table 3

Measuring Equipment	Impedance analyzer Agilent E4991A，Network analyzer Keysight E5071C or equivalent

Irms

Temperature rise no more than 40℃ against chip surface temperature when the allowable current (which is mentioned in item 5) is applied.

Isat

The inductance drop less than 30% when the current applied.

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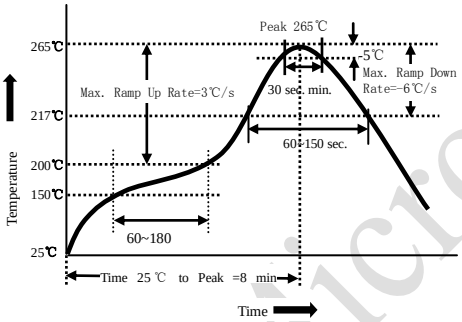
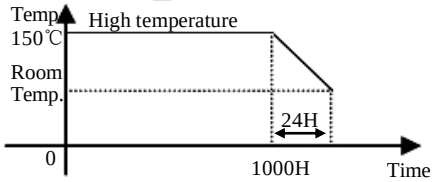
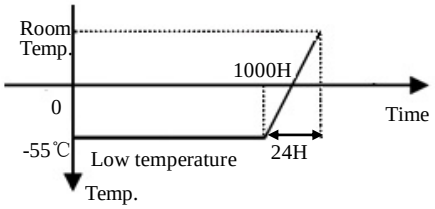
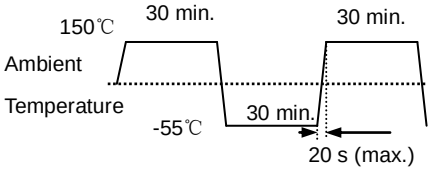
7. Reliability

No.	Item	Requirements	Test Methods and Remarks	Reference	Sample Size
1	Solderability	Terminal area shall be at least 95% covered .	①Temperature:240±5℃, flux 5-10 s. ②Sample immersion tin furnace 3 ±1s. ③Sn/3.0Ag/0.5Cu	AEC-Q200	15
2	Resistance to Soldering Heat	(1) No case deformation or change in appearance. (2)｜ΔL/L0｜≤5% (3)｜ΔQ/Q0｜≤20% (4)｜ΔDCR/DCR0｜≤10%	①The peak temperature: 260+5/-0℃. ②Reflow:3times. ③Temperature curve is as below: 	AEC-Q200	30
3	High Temperature Storage		①Temperature: 150±2℃. ②Time : 1000(+48,0) hours. ③Measurement at 24±4 hours after test conclusion. 	AEC-Q200	77
4	Low Temperature Storage		①Temperature: -55±2℃. ②Time : 1000(+48,0) hours. ③Measurement at 24±4 hours after test conclusion. 	JESD22-A119	77
5	Thermal shock		①First -55℃ for 30 minutes, last 150℃ 30minutes as 1 cycle. Go through 1000 cycles. ②Max transfer time is 20 second. ③Measurement at 24±4 hours after test conclusion. 	AEC-Q200	77

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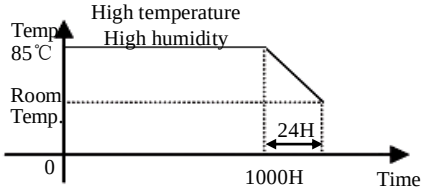
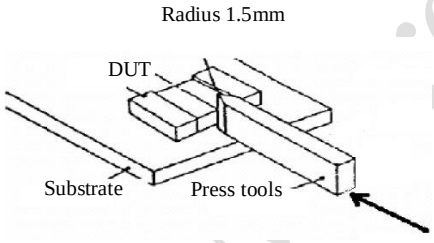
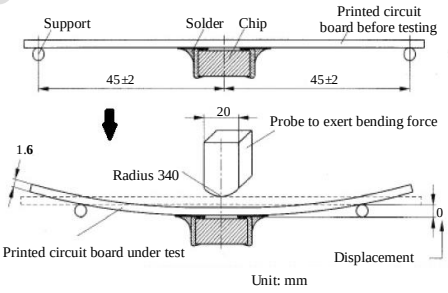
No.	Item	Requirements	Test Methods and Remarks	Reference	Sample Size
6	Humidity Resistance	(1) No case deformation or change in appearance. (2) | ΔL/L0 | ≤5% (3) | ΔQ/Q0 | ≤20% (4) | ΔDCR/DCR0 | ≤10%	①1000(+48,0) hours, 85 ℃/85%RH. ②Unpowered. ③Measurement at 24±4 hours after test conclusion. 	AEC-Q200	77
7	Terminal Strength	No case deformation or change in appearance.	①The test samples shall be soldered to the board. ②5N, 60±1s 	AEC-Q200	30
8	Board Flex	(1) No case deformation or change in appearance. (2) | ΔL/L0 | ≤5% (3) | ΔQ/Q0 | ≤20% (4) | ΔDCR/DCR0 | ≤10%	①Part mounted on a 100mm*40mm FR4 PCB board, which is 1.6mm thick and as a Layer-thickness 35 μm ±10 μm. ②Bending speed is 1mm/s. ③Keeping the P.C Board 2 mm minimum for 60 seconds. 	AEC-Q200	30
9	Vibration	(1) No case deformation or change in appearance. (2) | ΔL/L0 | ≤5% (3) | ΔQ/Q0 | ≤20% (4) | ΔDCR/DCR0 | ≤10%	10~2000Hz,5g,20min/Cycle,4 hours in each 3 mutually perpendicular directions (total of 12 hours)	AEC-Q200	30

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No.	Item	Requirements	Test Methods and Remarks	Reference	Sample Size
10	Mechanical Shock	(1) No case deformation or change in appearance. (2) | ΔL/L0 | ≤5% (3) | ΔQ/Q0 | ≤20% (4) | ΔDCR/DCR0 | ≤10%	Half sine shock pulse,100g,6ms,6 shocks in each 3 mutually perpendicular directions (total of 18 shocks)	AEC-Q200	30
11	Loading at High Temperature	(1) No case deformation or change in appearance. (2) | ΔL/L0 | ≤5% (3) | ΔQ/Q0 | ≤20% (4) | ΔDCR/DCR0 | ≤10%	①Temperature: 150±2℃. ②Time : 1000(+48,0) hours. ③Rated current. ④Measurement at 24±4 hours after test conclusion.	AEC-Q200	77
12	Loading at Damp Heat	(1) No case deformation or change in appearance. (2) | ΔL/L0 | ≤5% (3) | ΔQ/Q0 | ≤20% (4) | ΔDCR/DCR0 | ≤10%	①Temperature: 60±2℃, Humidity: 90% to 95% RH ; ② Duration: 1000(+48,0) hours ③ current: Rated current. ④Measurement at 24±4 hours after test conclusion.	AEC-Q200	77
13	ESD Test		HBM ESD discharge waveform,8KV	AEC-Q200	15

*All above experiments items need 3 Lot., sample size is as specified in the table above.

*Sample size standard is from AEC-Q200: qualification sample size requirements.

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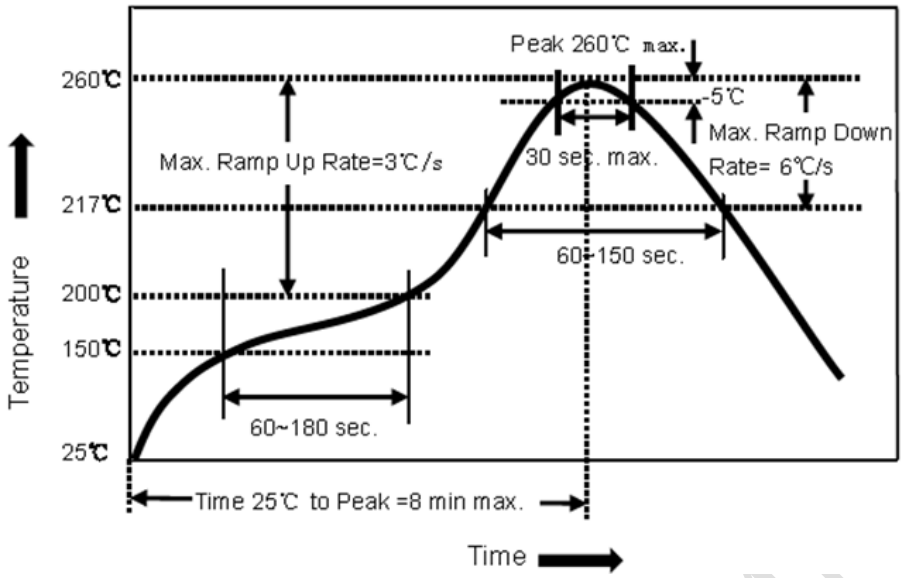
8. Recommended Soldering Conditions

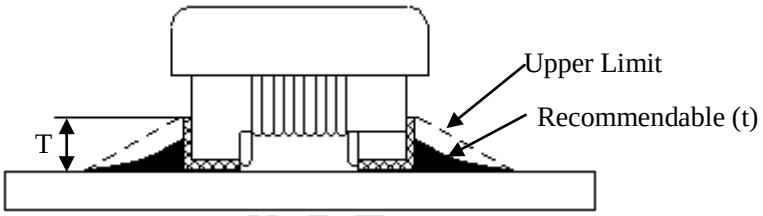
(1) Reflow soldering conditions



*Above reflow soldering curve is from J-STD-020D.

(2)Solder Volume

Solder shall be used not to be exceeded the upper limits as shown below.



1/3T≦t≦T

(T: Height of electrode)

a. Exceeding solder volume may cause the failure of mechanical or electrical performance.

b. Before soldering, please ensure that the solder should not adhere to the wire part of chip.

c. Please pay particular attention to whether there is flux remaining on surface of the wire part of chip after subjected to reflow soldering since this may causing short circuit of parts.

(3) Iron soldering

The following conditions must be strictly followed when using a soldering iron.

①

Pre-heating	150℃, 1 minute
Tip temperature	350℃ max
Soldering iron output	30w max
End of soldering iron	Φ1mm max
Soldering time	3 seconds max
Soldering times	3 max

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②Don’t touch the coil core directly with the top of the iron

③In the welding process, the electric iron cannot bump into the enamel-insulated wire, lest components should have evidence of damage.

④The test, link products and so on solder correct and support on both sides the method contrast wrongly:

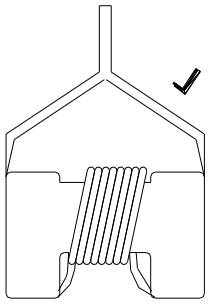

Figure 1 Correct method

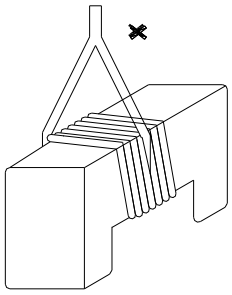

Figure 2 Wrongly method

Tweezers of fixture should support on both sides of the chip, and the correct support way as shown as Figure1. Tweezers of fixture should not support on enamel-insulated wire of the chip, lest enamel-insulated wire should have evidence of damage, the wrong support way as shown as Figure 2.

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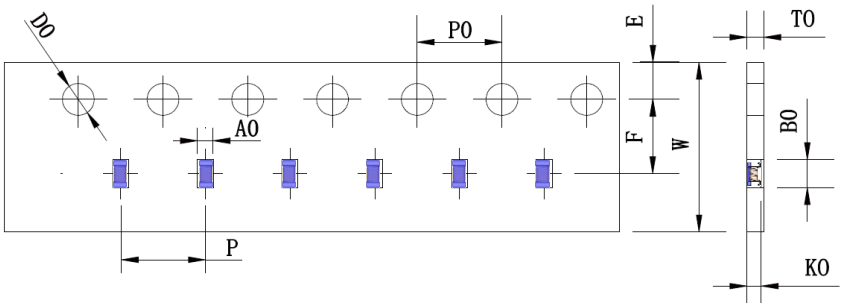
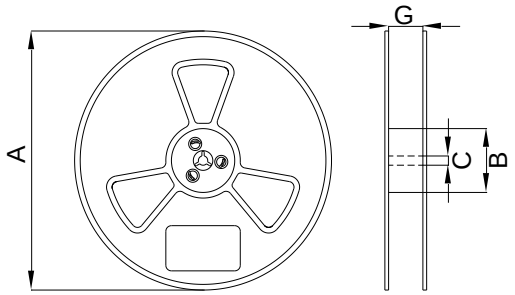
9.Packaging Information

(1) Dimension of tape (Unit: mm)



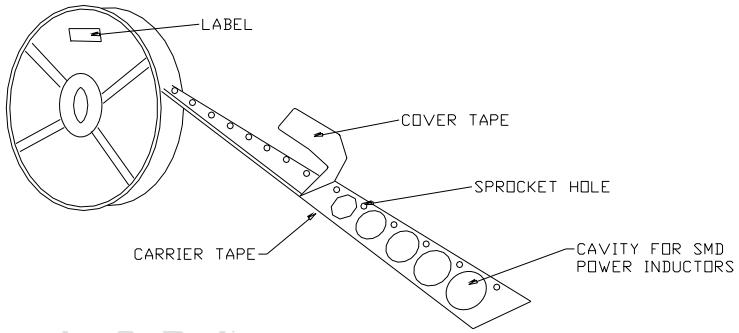
W	E	F	P	P0	D0	T0
8.0±0.2	1.75±0.05	3.5±0.05	4.0±0.05	4.0±0.1	1.5+0.1/-0.0	2.75 max.

(2) Dimension of reel (Unit: mm)



Symbol	Dimension
A	178±2
B	58±2
C	13.5±0.2
G	10.0±1.5

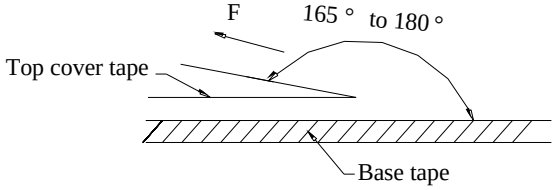
(3) Taping figure and drawing direction



(4) Packaging quantities: 1500PCS/Reel.

(5) Peeling strength of cover tape:

The force tearing off cover tape is 15 to 65 grams in the arrow direction under the following conditions.



Room Temp. (℃)	Room Humidity (%)	Room aim (hpa)	Peel Speed mm/min
5-35	45-85	860-1060	300

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10. Storage

a. The solder ability of the external electrode may be deteriorated if packages are stored where they are exposed to high temperature or high humidity. Besides, to ensure packing material's good state, packages must be stored at -40℃ to 125℃ and 15% ~85%RH.
b. The solder ability of the external electrode may be deteriorated if packages are stored where they are exposed to dust of harmful gas (e.g. HCl, sulfurous gas of H_2S).
c. Packaging materials may deform if packages are exposed directly to sunlight.
d. Minimum packages, such as polyvinyl heat-seal packages shall not be opened until they are used. If opened, use the reels as soon as possible.
e. Solderability shall be guaranteed for 12 months from the date of manufacture on condition that they are stored at the environment specified in specification. For those parts, which passed more than the time shall be checked solder-ability before use.

11. Transportation

The cases shall not be damaged, destroyed and rained on.

12. Warning and Attentions

(1) Precautions on Use

a. Always wear static control bands to protect against ESD.
b. Any devices used (soldering iron, measuring instruments) should be properly grounded.
c. Use non-magnetic tweezers when handing the chips.
d. Pre-heating when soldering, and refer to the recommended condition specified in specification.
e. Don't apply current in excess of the rated current value. It may cause damage to components due to over-current.
f. Keep clear of anything that may generate magnetic fields such as speakers, coils.
g. When soldering, the electrical characteristics may be varied due to hot energy and mechanical stress.
h. When coating products with resin, the relatively high resin curing stress may change the electrical characteristics. For exterior coating, select resin carefully so that electrical and mechanical performance of the product is not affected. Before using, please evaluate reliability with the product mounted in your application set.
i. When mount chips with adhesive in preliminary assembly, do appropriate check before the soldering stage, i.e., the size of land pattern, type of adhesive, amount applied, hardening of the adhesive on proper usage and amounts of adhesive to use.
j. Mounting density: Add special attention to radiating heat of products when mounting other components nearby. The excessive heat by other products may cause deterioration at joint of this product with substrate.
k. Since some products are constructed like an open magnetic circuit, narrow spacing between components may cause magnetic coupling.
l. Please do not give the product any excessive mechanical shocks in transportation.
m. Please do not touch wires by sharp terminals such as tweezers to avoid causing any damage to wires.
n. Please do not add any shock and power to the soldered product to avoid causing any damage to chip body.
o. Please do not touch the electrodes by naked hand as the solderability of the external electrodes may deteriorate by grease or oil on the skin.

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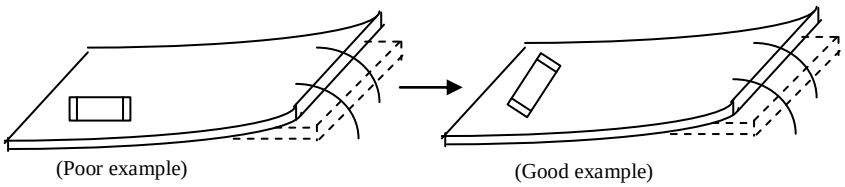
(2) PCB Bending Design

The following shall be considered when designing and laying out PCB's.

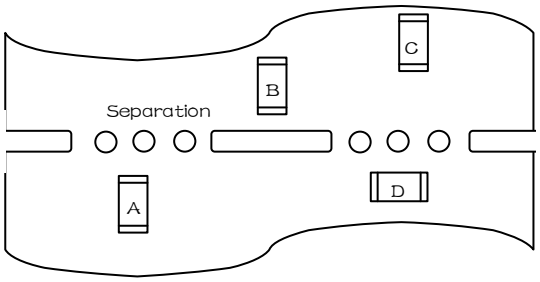
1. PCB shall be designed so that products are not subjected to the mechanical stress from board warp or deflection.



(Poor example) (Good example)

Products shall be located in the sideways direction to the mechanical stress.

2. Products location on PCB separation.



Product shall be located carefully because they may be subjected to the mechanical stress in order of A>C=B>D.

3. When splitting the PCB board, or insert (remove) connector, or fasten thread after mounting components, care is required so as not to give any stress of deflection or twisting to the board. Because mechanical force may cause deterioration of the bonding strength of electrode and solder, even crack of product body. Board separation should not be done manually, but by using appropriate devices.

(3) Recommended PCB Design for SMT Land-Patterns

When chips are mounted on a PCB, the amount of solder used (size of fillet) and the size of PCB Land-Patterns can directly affect chip performance. Therefore, the following items must be carefully considered in the design of solder land patterns.

a. Please use the PCB pad and solder paste we recommend, and contact us in advance if they need to be changed.
b. The amount of solder applied can affect the ability of chips to withstand mechanical stresses which may lead to breaking or cracking. Therefore, when designing land-patterns it is necessary to consider the appropriate size and configuration of the solder pads which in turn determines the amount of solder necessary to form the fillets.
c. When more than one part is jointly soldered onto the same land or pad, the pad must be designed that each component's soldering point is separated by solder-resist.

Recommended land dimensions please refer to product specification.

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13. Cleaning

Products shall be cleaned on the following conditions:

(1) Cleaning temperature shall be limited to 60℃Max. (40℃Max. for IPA)

(2)Ultrasonic cleaning shall comply with the following conditions, avoiding the resonance phenomenon at the mounted products and PCB.

Power: 20W/l Max. Frequency: 28 KHz to 40 KHz Time: 5 minutes Max

(3)Cleaner

a. Alcohol type cleaner

Isopropyl alcohol (IPA)

b. Aqueous agent

Surface Active Agent Type (Clean through-750H)

Hydrocarbon Type (Techno Cleaner-335)

Higher Alcohol Type (Pine Alpha ST-100S)

c. There shall be no residual flux and residual cleaner after cleaning.

In the case of using aqueous agent, product shall be dried completely after rinse with de-ionized water in order to remove the cleaner.

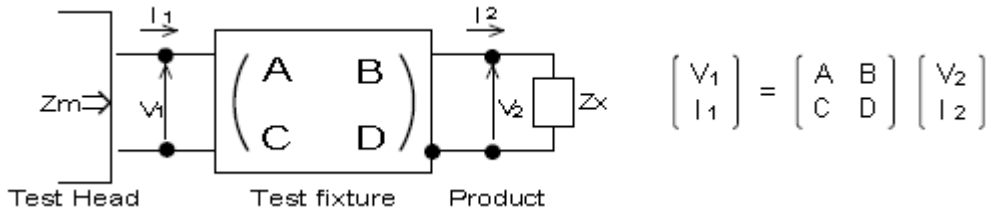
d. Some products may become slightly whitened. However, product performance or usage is not affected.

e. Please take care of winding part while cleaning.

f. After cleaning, parts could be subjected to the next reflow soldering till the solvent remaining on surface of parts being volatilized.

14. Measuring Method of Inductance

(1) Residual elements and stray elements of test fixture can be described by F-parameter shown in following.



$$\begin{bmatrix} V_1 \\ I_1 \end{bmatrix} = \begin{bmatrix} A & B \\ C & D \end{bmatrix} \begin{bmatrix} V_2 \\ I_2 \end{bmatrix}$$

(2) The impedance of chip coil Zx and measured value Zm can be described by input/output current/voltage.

$$Zm = \frac{V_1}{\quad} \qquad Zx = \frac{V_2}{\quad}$$

$$Zx = \alpha \frac{Zm-\beta}{1-Zm\Gamma}$$

where, $\alpha = D / A = 1$

$\beta = B / D = Zsm-(1-Yom\ Zsm)Zss$

$\Gamma = C / A = Yom$

(3) Lx and Qx shall be calculated with the following equation.

$$Lx = \frac{Im(Zx)}{2\pi f} \qquad Qx = \frac{Im(Zx)}{Re(Zx)}$$

Lx: Inductance of chip coil

Qx: Q of chip coil

f: Measuring frequency

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